

Patent

Attorney Docket No.: Intel 2207/1012302  
Assignee: Intel Corporation

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Sailesh KOTTAPALLI et. al.

SERIAL NO. OF  
PARENT APPLICATION : 09/753,060

FILING DATE OF  
PARENT APPLICATION : December 29, 2000

FOR : METHOD AND APPARATUS FOR IMPROVING  
DISPERSAL PERFORMANCE IN A PROCESSOR  
THROUGH THE USE OF NO-OP PORTS

ART UNIT OF  
PARENT APPLICATION : 2183

EXAMINER OF  
PARENT APPLICATION : William M. TREAT

M/S: PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV351181305US in an envelope addressed to: M/S: PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Dated: <u>March 19, 2004</u>	Signature  Barbara Vance

**PRELIMINARY AMENDMENT**

Dear Sir:

Prior to examination, please amend the above-identified continuation application as follows. This application is a Continuation of U.S. Patent Application Serial No. 09/753,060 filed on December 29, 2000.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.